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detecting said digital data and passing [some of] said digital data to said processor;

generating and communicating [some of the] said video image [of said television program] in response to said [detected and passed] digital data;

inputting a clear-and-continue signal to said processor in response to said digital data detected in said television signal;

controlling said processor based on said clear-and-continue signal, said step of controlling comprising the steps of:

- [(1)](a) clearing at least [some] a portion of an output memory;
- [(2)](b) jumping to a predetermined instruction; and
- [(3)](c) [commencing or recommencing] generating video image information based on said predetermined instruction.

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3. (Amended) The method of claim 2, wherein said detected and passed digital data include a computer program, said method further comprising the steps of:

storing said computer program at a memory associated with said processor; and

determining an address at said memory to jump to.

4. (Amended) The method of claim 2, wherein a processor interrupt signal causes said processor to respond to said clear-and-continue signal at a specific time, said method further [having one step from the group] consisting of:

detecting [a] said processor interrupt signal in [a] said television signal;

selecting [a] said processor from a plurality of processors to interrupt based on data detected in [a] said television signal; and

communicating said clear-and-continue signal [as a] with said processor interrupt signal.

5. (Amended) The method of claim 2, wherein said clear-and-continue signal is inputted to said processor by a controller, said method further comprising the steps of:

inputting data detected in said television signal to said controller; and
communicating signals from said controller to said processor based on said inputted data.

6. (Amended) A method of generating a television display at at least one of a plurality of receiver stations, each of said plurality of receiver stations having a television monitor for displaying television programming and a processor for generating and communicating [at least some of] a video image [of said television programming] to said television monitor, comprising the steps of:

[(1)](a) receiving a clear-and-continue signal;

[(2)](b) receiving a control signal which operates at a transmitter station to communicate said clear-and-continue signal to a transmitter; and

[(2)](c) transmitting said clear-and-continue signal, said clear-and-continue signal effective at said at least one of a plurality of receiver stations to control said processor to clear at least [some] a portion of an output memory, jump to a